	Type	L #	Hits	Search Text	DBs
3	BRS	L3	871	(terminat$3 near2 enabl$3) and resistor and (switch$3 transistor pmos nmos fet mosfet)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
4	BRS	L4	795	3 and (enable)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
5	BRS	L5	550	4 and mode	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
6	BRS	L6	349	5 and memory	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
7	BRS	L7	63	3 and (mode adj3 register)	USPAT